&TDK Multilayer Ceramic Chip Capacitors

C1005X6S1C155M050BC



TDK item description C1005X6S1C155MT****

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C1005 [EIA 0402]	
Status	Production	



Size		
Length(L)	1.00mm +0.15,-0.10mm	
Width(W)	0.50mm +0.15,-0.10mm	
Thickness(T)	0.50mm +0.15,-0.10mm	
Terminal Width(B)	0.10mm Min.	
Terminal Spacing(G)	0.30mm Min.	
Recommended Land Pattern (PA)	0.30mm to 0.50mm	
Recommended Land Pattern (PB)	0.35mm to 0.45mm	
Recommended Land Pattern (PC)	0.40mm to 0.60mm	

Electrical Characteristics		
Capacitance	1.5μF ±20%	
Rated Voltage	16VDC	
Temperature Characteristic	X6S(±22%)	
Dissipation Factor (Max.)	10%	
Insulation Resistance (Min.)	66MΩ	

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	10000pcs

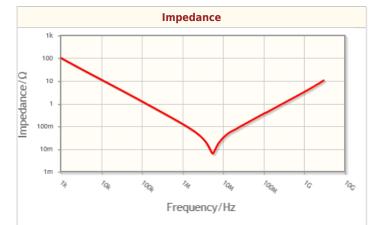
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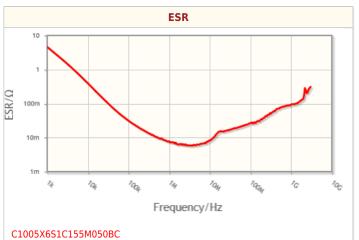
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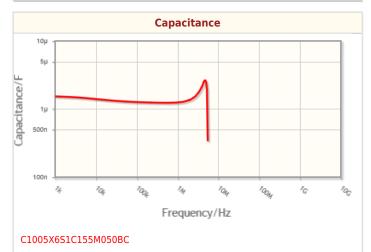


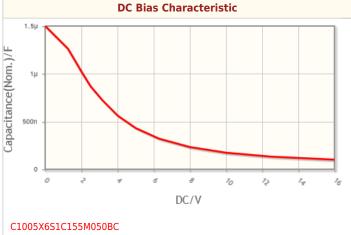






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Ripple Temperature Rising 20 Temperature Rise/°C 15 10 5 0 0.4 0 0.8 د.9 0.6 ډ.′ 7.₄ 7.6 7.₀ Ripple Current/Arms C1005X6S1C155M050BC(100kHz) C1005X6S1C155M050BC(500kHz) C1005X6S1C155M050BC(1MHz)

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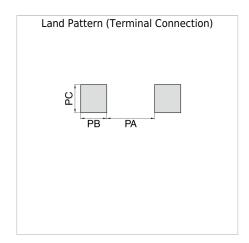
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Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

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Associated Images



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